

Varistor

Material Data Sheet

Product Class	Hicap SHCV / X7R B725xx*
Date	14.10.2019
IMDS ID if available	
Version	5.06

Product Part (IMDS: semi component)	Material Class (IMDS: Material)	Material (Classification) VDA 231	Substance	TMPS** [wt%]	CAS if applicable	typical mass of material [wt-%]	Traces see 1)
Active Part - MLV	Ceramic	3B	ZnO	89	1314-13-2	24.3	
			Bi2O3	3	1304-76-3		
			Sb2O3	5	1309-64-4		
			Co3O4	1	1308-06-1		
			NiO	1	1313-99-1		
			others*)	1			
Inner electrode - MLV	Noble Metal	1D7	Ag	75-90	7440-22-4	0.4	
			Pd	10-25	7440-05-3		
Termination - MLV	Composite	4D	Ag	70	7440-22-4	2	
			Pd	20	7440-05-3		
			Glass frit (lead-boro-silicate) or Ag	10 93	7440-22-4		
			Glass frit (lead-boro-silicate)	7			
Active Part - MLCC	Ceramic	3B	BaTiO3	96		20.7	
			others*)	4			
Inner electrode - MLCC	Heavy Metal	1C14	Ni	100	7440-02-0	0.7	
Termination - MLCC	Composite	4D	Cu	94	7440-50-8	2.3	
	Heavy Metal	1C8	Sn	100	7440-31-5	0.4	
	Heavy Metal	1C14	Ni	100	7440-02-0	0.2	
Solder	Heavy Metal	1C15	Sn	1	7440-31-5	11	
			Pb +)	98	7439-92-1		
			Ag	1	7440-22-4		
Leads	Iron & Steel incl. Alloys	1A	Fe	100	7439-89-6	28.2	
	Heavy Metal	1C8	Sn	100	7440-31-5	1.2	
	Heavy Metal	1C14	Ni	100	7440-02-0	0.6	
Encapsulation	Duromer	2D	SiO2	49	60676-86-0	8	
			Epoxy	35	25068-38-6		
			Brominated epoxy	12	68929-70-1		
			Sb2O3	2.5	1309-64-4		
			others*)	1.5			
Sum of total						100	

sizes [mm]	weight range [g]	material numbers	part numbers
7.3 x 7.8 x 3.7	0.35	B72587*	SR1
7.8 x 9.0 x 4.1	0.65	B72547*	SR2

Not part of a Product Class	
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Contact	Ronner Christoph	Important remarks: 1) The declaration limit is 0.1% as defined by IEC 62474 (IEC PAS 61906). Traces are product parts, substances etc. that are below a percentage of 0.1 % by weight, if not otherwise regulated. 2) This Material Data Sheet contains typical values of the respective products set forth herein. We expressly point out that all values and statements contained herein are based on our best present knowledge and cannot be regarded as binding statements or binding product specifications, unless otherwise explicitly agreed in writing. TDK ELECTRONICS AG AND ITS AFFILIATES HEREBY EXPRESSLY DISCLAIM ANY REPRESENTATION OR WARRANTY, WHETHER EXPRESS, IMPLIED OR STATUTORY, WITH REGARD TO THE STATEMENTS AND VALUES CONTAINED HEREIN, INCLUDING BUT NOT LIMITED TO ANY REPRESENTATION OR WARRANTY OF MERCHANTABILITY OR SUITABILITY FOR ANY PURPOSE.
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*) others: (not declarable or prohibited substances acc. GADSL) **) typical mass percentage of substance +) listed in the Candidate list of Substances of Very High Concern acc. to Regulation 1907/2006/EC		

The products set forth herein are "RoHS-compatible". RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

RoHS - Exemptions for the Product Class / Product according to Annex III: (valid not valid)

- no exemptions;**
- Exemption 6 (a): Lead as an alloying element in steel for machining purposes and in galvanized steel containing up to 0,35 % lead by weight;
- Exemption 6 (b): Lead as an alloying element in aluminium containing up to 0,4 % lead by weight;
- Exemption 6 (c): Copper alloy containing up to 4 % lead by weight;
- Exemption 7 (a): Lead in high melting temperature type solder (i.e. lead-based alloys containing 85 % by weight or more lead);
- Exemption 7 (c)-I: Electrical and electronic components containing lead in a glass or ceramic other than dielectric ceramic in capacitors, e.g. piezoelectronic devices, or in a glass or ceramic matrix compound;
- Exemption 7 (c)-II: Lead in dielectric ceramic in capacitors for a rated voltage of 125 V AC or 250 V DC or higher;
- Exemption 7 (c)-III: Lead in dielectric ceramic in capacitors for a rated voltage of less than 125 V AC or 250 V DC;
- Exemption 15: Lead in solders to complete a viable electrical connection between semiconductor die and carrier within integrated circuit Flip Chip packages;
- Other Exemption than above